



Masood Nekoie

MASTER STUDENT IN TELECOMMUNICATION ENGINEERING

Education

Master of Telecommunication Engineering, Amirkabir University of Technology (Tehran Polytechnic), Tehran

SEPTEMBER 2017 – AUGUST 2019

GPA: (3.8/4)

Selected Courses:

- Microwave measurements (18.3/20)
- Radio Network Design (17/20)

Bachelor of Telecommunication Engineering, Isfahan University of Technology, Isfahan

SEPTEMBER 2013 – AUGUST 2017

Selected Courses:

- Microwave 2 (18.3/20)

Publication

Masood nekoie, Seyed Amirhossein Moghaddas, Emadaldin Mohammadi Golafshani, Amir H. Gandomi, Introduction of artificial bee colony expression programming as an automatic programming method, Information Sciences, Published

APRIL 2019 – PRESENT

<https://doi.org/10.1016/j.ins.2020.09.020>

Masood nekoie, Gholamreza Moradi, Emadaldin Mohammadi Golafshani, Application of Multigen Genetic Expression Programming and Artificial Neural Network in electric and magnetic material characterization, In-Preparation

Masood nekoie, Gholamreza Moradi, Reza sarraf shirazi, Analysis & methodology of extracting permittivity constant of lossy materials in THz band using ABCEP as an automated programming method, In-Preparation

NOVEMBER 2020 – NOVEMBER 2020

Academic Experience

Teacher Assistant in Telecommunication Circuits Laboratory, Amirkabir University of Technology (Tehran Polytechnic)

MARCH 2019 – JULY 2020

- Oscillator Colpitts Designing and implementation
- AM modulator and demodulator design and implementation
- FM modulator and demodulator design and implementation

Teacher Assistant in Logic Circuits Lab, Amirkabir University of Technology (Tehran Polytechnic)

SEPTEMBER 2018 – MARCH 2019

- UART & USB implementation on FPGA Spartan 6

Details

Experienced in software development (MATLAB, RF Design, AVR, FPGA and Arduino)- Teacher Assistant in Telecommunication Circuits, Logic Circuits and FPGA Laboratory. Published the Introduction of artificial bee colony expression programming as an automatic programming method, Information Sciences (+98)9133239810 masoodnekoie@gmail.com

Links

[Linkedin](#)

[Website](#)

Skills

Simulink

MATLAB Programming

C programming

ISE Design suite

ModelSim

CST Studio suite

Python

Vivado Design suite

Languages

English

Persian

- VGA implementation by FPGA Spartan 3
- Image processing by FPGA Spartan 6
- MATLAB & FPGA communication

Teacher Assistant in FPGA Lab, Amirkabir University of Technology (Tehran Polytechnic)

SEPTEMBER 2018 – MARCH 2019

- Introduction to Verilog & VHDL
- Introduction to ISE design suite
- Introduction to FPGA
- FPGA implementation

Research Interest

Automatic programming techniques meta-heuristic algorithms

Swarm-intelligence Algorithms

Optimization Algorithms

Employment History

MATLAB, AVR, FPGA and Arduino developers, Spadana Smart Communications Development Company

JUNE 2019 – PRESENT

<http://intelicom.ir/>

Duties:

- Design of face recognition systems for use in the public transportation system
- Set up faulty network systems such as routers and switches
- Collaborated on high priority projects with tact and strong attention to detail.

Designer of RF, analog and digital telecommunication systems and FPGA programmer, Mobin Saman technology

APRIL 2018 – MAY 2019

<http://www.mobinsaman.com/>

Duties:

- Designing of RF and digital circuits
- Programming, testing, and commissioning of circuits
- Checking PLL system and amplifiers

References

Dr. Gholamreza Moradi from Supervisor of M.Sc Thesis

ghmoradi@aut.ac.ir

Dr. Emadaldin Mohammadi Golafshani from collaboration in the published article

golafshani@srbiau.ac.ir